HOW TO:
Integrate an optical system into a chip

1. System concept
   - Definition of optical functionalities

2. Optical architecture
   - Mapping of concept into photonic technologies and components

3. Chip architecture
   - Implementation of photonic components with design building blocks

4. Chip design
   - Photonic integrated circuit layout

5. Performance simulation
   - Evaluation and optimization of the designed photonic circuit

6. Mask layout + DRC
   - Delivery to selected foundry and design rule check with available PDK

7. Manufacture
   - Wafer procurement, mask fabrication, lithography, etching, metallization, dicing, etc.

8. Chip testing
   - Evaluation of physical manufacture and functional specifications

9. Packaging
   - Encapsulation of selected chips in connectorized housings

More info: www.vlcphotonics.com